

MEDIA ALERT



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SmartDV Heads to ChipEx2019 in Israel with Extensive Design and Verification IP Portfolio

Will Feature Compatibility Across All Verification Languages, Platforms, Methodologies

SAN JOSE, CALIF — May 7, 2019 —

WHO: [SmartDV™ Technologies](#), the ***Proven*** and ***Trusted*** choice for Verification

Intellectual Property (VIP), Design IP and verification services

WHAT: Will feature its extensive portfolio of Verification IP and Design IP compatible with all verification languages, platforms and methodologies at [ChipEx2019](#), the annual conference of the Israeli Microelectronics Industry. Also highlighted will be SmartDV's proprietary SmartCompiler™ that automates rapid creation and customization of its Design and Verification IP in use at more than 120 leading networking, storage, wireless and memory companies.

WHEN: Monday, May 13

WHERE: Tel Aviv Convention Center, Tel Aviv, Israel

About SmartDV

[SmartDV™ Technologies](#) is the Proven and Trusted choice for Verification and Design IP with the best customer service from more than 250 experienced ASIC and SoC design and verification engineers. Its high-quality standard or custom protocol Verification and Design IP is compatible with all verification languages, platforms and methodologies supporting all simulation, emulation and formal verification tools used in a coverage-driven chip design verification flow. The result is Proven and Trusted Verification and Design IP used in hundreds of networking, storage, automotive, bus, MIPI and display chip projects throughout the global electronics industry. SmartDV is headquartered in Bangalore, India, with U.S. headquarters in San Jose, Calif. Visit [SmartDV](#) to learn more.

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